

**AMENDMENT TO CLAIMS**

1. (Withdrawn) A semiconductor device comprising:  
a Group III nitride semiconductor layer; and  
a gate electrode formed on the Group III nitride semiconductor layer,  
the gate electrode containing an adhesion enhancing element,  
a thermally oxidized insulating film being interposed between the Group III nitride semiconductor layer and the gate electrode.
2. (Withdrawn) The semiconductor device of claim 1, wherein the thermally oxidized insulating film is composed of an aluminum oxide or a silicon oxide.
3. (Withdrawn) The semiconductor device of claim 1, wherein the thermally oxidized insulating film has a thickness not less than 0.5 nm and not more than 3 nm.
4. (Withdrawn) The semiconductor device of claim 1, wherein the gate electrode contains Pd.
5. (Withdrawn) The semiconductor device of claim 1, wherein the adhesion enhancing element is a highly oxidizable element.
6. (Withdrawn) The semiconductor device of claim 1, wherein the adhesion enhancing element is Ti, Ni, or Si.

7. (Withdrawn) The semiconductor device of claim 1, wherein the thermally oxidized insulating film is an insulating film obtained by thermally oxidizing the Group III nitride semiconductor layer.

8. (Withdrawn) The semiconductor device of claim 7, wherein the adhesion enhancing element is Si and a weight percent of the Si in metals composing the gate electrode is not less than 3% and not more than 10%.

9. (Withdrawn) The semiconductor device of claim 7, wherein the adhesion enhancing element is Si and a weight percent of the Si in metals composing the gate electrode is not less than 4% and not more than 7%.

10. (Currently amended) A method for fabricating a semiconductor device, the method comprising the steps of:

thermally oxidizing a Group III nitride semiconductor layer to form a thermally oxidized insulating film on a surface of the Group III nitride semiconductor layer; and

forming a gate electrode containing an adhesion enhancing element and an element other than the adhesion enhancing element on the thermally oxidized insulating film.

11. (Original) The method of claim 10, wherein the step of forming the thermally oxidized insulating film includes the step of:

forming an aluminum nitride layer on the Group III nitride semiconductor layer and then thermally oxidizing the aluminum nitride layer to change the aluminum nitride layer into an

aluminum oxide layer and thereby form the thermally oxidized insulating film composed of the aluminum oxide layer.

12. (Original) The method of claim 11, wherein the aluminum oxide layer has a thickness not less than 0.5 nm and not more than 3 nm.

13. (Withdrawn) The method of claim 10, wherein the step of forming the thermally oxidized insulating film includes the step of:

forming a silicon layer on the surface of the Group III nitride semiconductor layer and then thermally oxidizing the silicon layer to change the silicon layer into a silicon oxide layer and thereby form the thermally oxidized insulating film composed of the silicon oxide layer.

14. (Withdrawn) The method of claim 13, wherein the silicon oxide layer has a thickness not less than 0.5 nm and not more than 3 nm.

15. (New) The method of claim 10, wherein the adhesion enhancing element is Ti, Ni, or Si.

16. (New) The method of claim 10, wherein the adhesion enhancing element is Si and a weight percent of the Si in metals composing the gate electrode is not less than 3% and not more than 10%.

17. (New) The method of claim 10, wherein the adhesion enhancing element is Si and a weight percent of the Si in metals composing the gate electrode is not less than 4% and not more than 7%.